

November 11-13, 2022 | Shenzhen, China

## CONFERENCE INTRODUCTION

<http://www.apccas2022.org/cfss.html>

The IEEE Asia Pacific Conference on Circuits and Systems (APCCAS) is the regional flagship conference of the IEEE Circuits and Systems Society in Asia. APCCAS 2022 will be held in Shenzhen, China during November 11-13, 2022. Shenzhen is the electronics R&D world center with more than 300 global or regional centers from Fortune Global 500 companies. Besides, Shenzhen is also suitable for tourism, which is ranked No. 2 in the list of best cities for travel by Lonely Planet.

## SPECIAL SESSION ON

Session Title	<b>Design for Test on Memory, Circuit, and Device</b>
Special Session Organizer	<b>Li Du, Nanjing University Chang Liang, University of Electronic Science and Technology of China</b>

### ABSTRACT

The reliability and stability of memory, circuit and device are significantly for the integrated circuit design. Particularly, the circuit used in the space application, aerospace electronic systems, specific electronic applications, are susceptible by the space environment. Various technologies have been employed into the specific circuit design and device research. In this session, we invite six papers to discuss the reliability and stability issues on memory, circuit, and device. Also, several good solutions have been proposed. We present the SEU tolerated read circuit for STT-MRAM, the endurance predict model for RRAM, and hardness assurance of de-latching and self-recovery, which are for the memory circuit. In addition, we discuss the single event burnout (SEB) effect is investigated for 18V LDMOS, the multi-beam interference model for equivalent heavy-ion linear energy transfer of pulsed laser energy, high-efficiency aging test technique for both device and circuits.

## IMPORTANT DATES

Submission of Special Session Full Papers

August 6, 2022

## PUBLICATION

Accepted papers will be submitted for inclusion into IEEE Xplore subject to meeting IEEE Xplore's scope and quality requirements. Selected papers will be invited to submit extended full papers to IEEE Transaction on Circuits and Systems I (TCAS-I), IEEE Transaction on Circuits and Systems II (TCAS-II) and IEEE Open Journal of Circuits and Systems (OJCAS).

## SUBMISSION METHOD



WORD

Papers for publication must be submitted in full paper electronically. All edition and update must be done up until the submission deadline. Papers submitted to APCCAS Conference will undergo a double-blind review process. Submissions must have all details identifying the author(s) removed from the original manuscript (including authors' names, affiliations, acknowledgments and other related personal information).

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LATEX



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